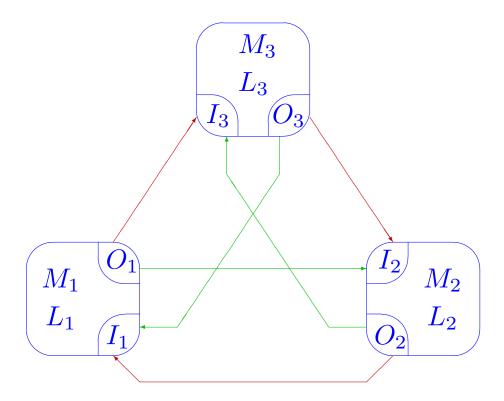
# Target Based Accepting Networks of Evolutionary Processors with Regular Filters

#### Bianca Truthe

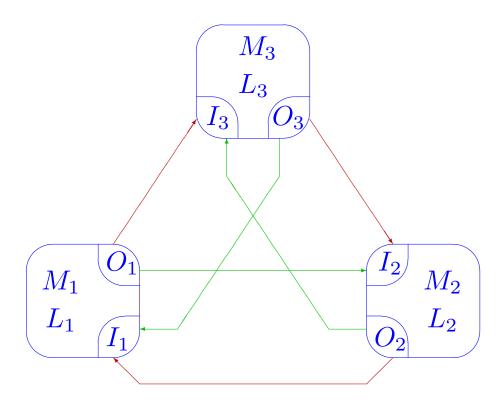
Otto-von-Guericke-Universität Magdeburg, Germany truthe@iws.cs.uni-magdeburg.de

Workshop on Non-Classical Models of Automata and Applications August 31 – September 1, 2009, Wrocław, Poland

# Introduction

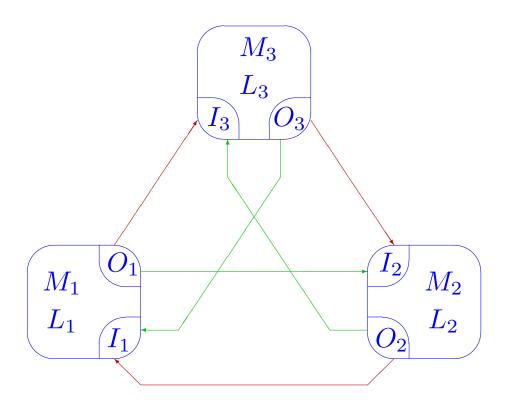


## Introduction



- E. Csuhaj-Varjú, A. Salomaa: In New Trends in Formal Languages, 1997
- J. Castellanos, C. Martín-Vide, V. Mitrana, J. Sempere: In LNCS 2084, 2001

#### Introduction



- E. Csuhaj-Varjú, A. Salomaa: In New Trends in Formal Languages, 1997
- J. Castellanos, C. Martín-Vide, V. Mitrana, J. Sempere: In LNCS 2084, 2001
- A. Alhazov, J. Dassow, C. Martín-Vide, Y. Rogozhin, B. Truthe: Fundamenta Informaticae 91 (2009)
- J. Dassow, V. Mitrana: NCGT'08 V. Mitrana, I
- V. Mitrana, B. Truthe: LATA'09

ANEP: 
$$\mathcal{N} = (U, V, N_1, N_2, \dots, N_n, E, j, O)$$

Processor:  $N_i = (M_i, I_i, O_i)$ 

substituting:  $M_i \subseteq \{ a \rightarrow b \mid a, b \in V \}$ 

deleting:  $M_i \subseteq \{ a \to \lambda \mid a \in V \}$ 

inserting:  $M_i \subseteq \{ \lambda \to b \mid b \in V \}$ 

```
ANEP: \mathcal{N} = (U, V, N_1, N_2, \dots, N_n, E, j, O)
```

Processor:  $N_i = (M_i, I_i, O_i)$ 

substituting:  $M_i \subseteq \{ a \rightarrow b \mid a, b \in V \}$ 

deleting:  $M_i \subseteq \{ a \to \lambda \mid a \in V \}$ 

inserting:  $M_i \subseteq \{ \lambda \to b \mid b \in V \}$ 

Configuration:  $C_t^w = (C_t^w(1), C_t^w(2), \dots, C_t^w(n)) [C_0^w(j) = \{w\}, C_0^w(i) = \emptyset]$ 

ANEP: 
$$\mathcal{N} = (U, V, N_1, N_2, \dots, N_n, E, j, O)$$

Processor: 
$$N_i = (M_i, I_i, O_i)$$

substituting: 
$$M_i \subseteq \{ a \rightarrow b \mid a, b \in V \}$$

deleting: 
$$M_i \subseteq \{ a \to \lambda \mid a \in V \}$$

inserting: 
$$M_i \subseteq \{ \lambda \to b \mid b \in V \}$$

Configuration: 
$$C_t^w = (C_t^w(1), C_t^w(2), \dots, C_t^w(n)) [C_0^w(j) = \{w\}, C_0^w(i) = \emptyset]$$

Evolution: 
$$C_{2t}^w(i) \Longrightarrow^{M_i} C_{2t+1}^w(i)$$

Communication: 
$$C^w_{2t+2}(i) = C^w_{2t+1}(i) \setminus O_i \cup \bigcup_{(k,i)\in E} C^w_{2t+1}(k) \cap O_k \cap I_i$$

ANEP: 
$$\mathcal{N} = (U, V, N_1, N_2, \dots, N_n, E, j, O)$$

Processor:  $N_i = (M_i, I_i, O_i)$ 

substituting:  $M_i \subseteq \{ a \rightarrow b \mid a, b \in V \}$ 

deleting:  $M_i \subseteq \{ a \to \lambda \mid a \in V \}$ 

inserting:  $M_i \subseteq \{ \lambda \to b \mid b \in V \}$ 

Configuration:  $C_t^w = (C_t^w(1), C_t^w(2), \dots, C_t^w(n)) [C_0^w(j) = \{w\}, C_0^w(i) = \emptyset]$ 

Evolution:  $C_{2t}^w(i) \Longrightarrow^{M_i} C_{2t+1}^w(i)$ 

Communication:  $C^w_{2t+2}(i) = C^w_{2t+1}(i) \setminus O_i \cup \bigcup_{(k,i)\in E} C^w_{2t+1}(k) \cap O_k \cap I_i$ 

Computation:  $C_0 \Longrightarrow C_1 \vdash C_2 \Longrightarrow C_3 \vdash \cdots$ 

ANEP: 
$$\mathcal{N} = (U, V, N_1, N_2, \dots, N_n, E, j, O)$$

Processor: 
$$N_i = (M_i, I_i, O_i)$$

substituting: 
$$M_i \subseteq \{ a \rightarrow b \mid a, b \in V \}$$

deleting: 
$$M_i \subseteq \{ a \to \lambda \mid a \in V \}$$

inserting: 
$$M_i \subseteq \{ \lambda \to b \mid b \in V \}$$

Configuration: 
$$C_t^w = (C_t^w(1), C_t^w(2), \dots, C_t^w(n)) [C_0^w(j) = \{w\}, C_0^w(i) = \emptyset]$$

Evolution: 
$$C_{2t}^w(i) \Longrightarrow^{M_i} C_{2t+1}^w(i)$$

Communication: 
$$C^w_{2t+2}(i) = C^w_{2t+1}(i) \setminus O_i \cup \bigcup_{(k,i)\in E} C^w_{2t+1}(k) \cap O_k \cap I_i$$

Computation: 
$$C_0 \Longrightarrow C_1 \vdash C_2 \Longrightarrow C_3 \vdash \cdots$$

Language accepted: 
$$L(\mathcal{N}) = \{ w \in U^* \mid \exists t \geq 0 \, \exists o \in O : C^w_t(o) \neq \emptyset \}$$

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



 $A \to BC$ : uBCv

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $u\mathbf{p_1}Cv$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $up_1 C v$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $up_1 p_2 v$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

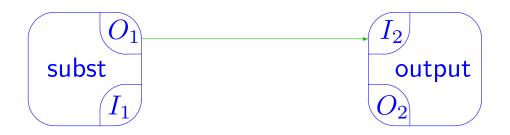
Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $u_{p_1}p_2v$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $up_3p_2v$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $up_3 p_2 v$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $up_3 p_4 v$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $up_3p_4v$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $uA p_4 v$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

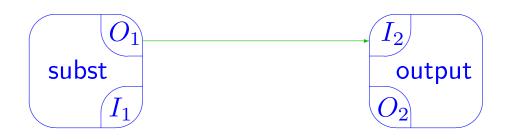
Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $uA \frac{p_4 v}{p_4}$ 

Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$A \to BC$$
:  $uA = v$ 

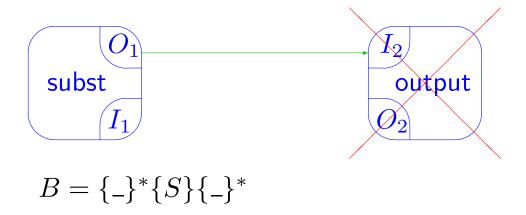
Theorem: For accepting any context-sensitive language, one substituting processor and one output processor are sufficient.

Proof: NEP with 1 substituting node and 1 output node simulating backwards a CS grammar in Kuroda normal form



$$I_2 = \{-\}^* \{S\} \{-\}^*$$

## **New Idea**



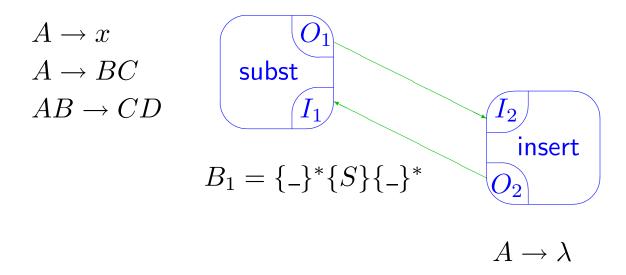
#### **Substitution and Insertion**

Theorem: Any recursively enumerable language can be accepted by a network of one substituting processor and one inserting processor.

#### **Substitution and Insertion**

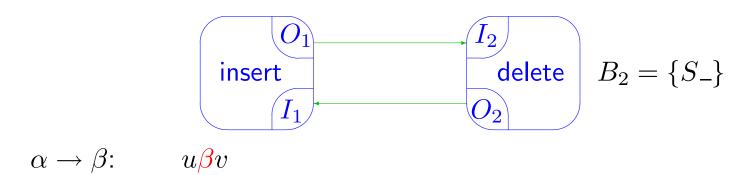
Theorem: Any recursively enumerable language can be accepted by a network of one substituting processor and one inserting processor.

Proof: NEP with 1 substituting node, 1 inserting node and 1 output node simulating backwards an RE grammar in Kuroda normal form

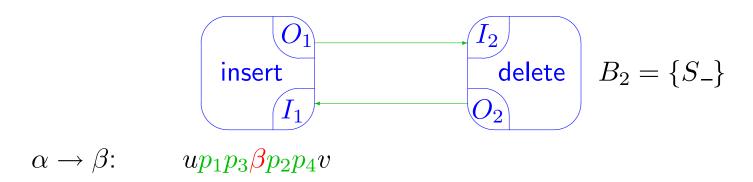


Theorem: Any recursively enumerable language can be accepted by a network of one inserting processors and one deleting processor.

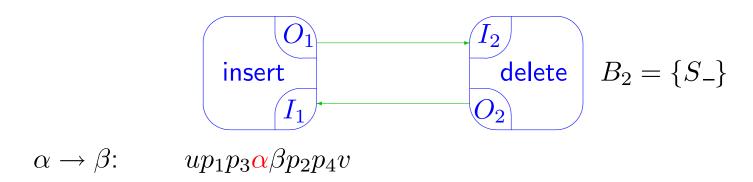
Theorem: Any recursively enumerable language can be accepted by a network of one inserting processors and one deleting processor.



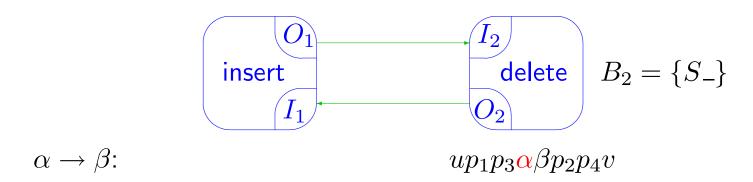
Theorem: Any recursively enumerable language can be accepted by a network of one inserting processors and one deleting processor.



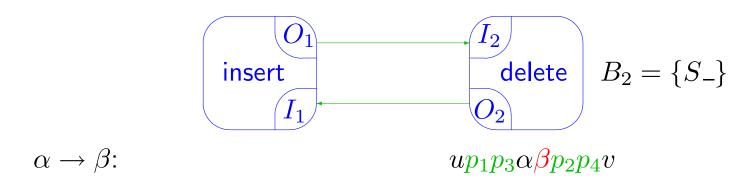
Theorem: Any recursively enumerable language can be accepted by a network of one inserting processors and one deleting processor.



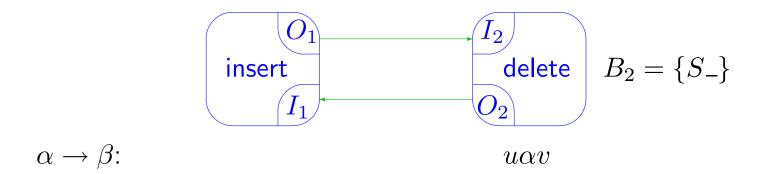
Theorem: Any recursively enumerable language can be accepted by a network of one inserting processors and one deleting processor.



Theorem: Any recursively enumerable language can be accepted by a network of one inserting processors and one deleting processor.



Theorem: Any recursively enumerable language can be accepted by a network of one inserting processors and one deleting processor.



# **Definition of Target Based ANEPs**

TB-ANEP:  $\mathcal{N} = (U, V, N_1, N_2, \dots, N_n, E, j)$ 

Processor:  $N_i = (M_i, I_i, O_i, B_i)$ 

 $B_i \subseteq V^*$  is called target set

# **Definition of Target Based ANEPs**

TB-ANEP: 
$$\mathcal{N} = (U, V, N_1, N_2, \dots, N_n, E, j)$$

Processor:  $N_i = (M_i, I_i, O_i, B_i)$ 

 $B_i \subseteq V^*$  is called target set

#### Language accepted:

$$L(\mathcal{N}) = \{ w \in U^* \mid \exists t \ge 0 \,\exists o : 1 \le o \le n \text{ and } C_t^w(o) \cap B_o \ne \emptyset \}$$

## **Definition of Target Based ANEPs**

TB-ANEP: 
$$\mathcal{N} = (U, V, N_1, N_2, \dots, N_n, E, j)$$

Processor:  $N_i = (M_i, I_i, O_i, B_i)$ 

 $B_i \subseteq V^*$  is called target set

Language accepted:

$$L(\mathcal{N}) = \{ w \in U^* \mid \exists t \ge 0 \,\exists o : 1 \le o \le n \text{ and } C_t^w(o) \cap B_o \ne \emptyset \}$$

Theorem: Every conventional network can be transformed into a target based network that accepts the same language.

# **Equivalence**

Theorem: Every target based network can be transformed into a conventional network that accepts the same language.

### **Equivalence**

Theorem: Every target based network can be transformed into a conventional network that accepts the same language.

TB-ANEP is called acceptance uniform if all nodes  $N_o = (M_o, I_o, O_o, B_o)$  with  $B_o \neq \emptyset$  satisfy  $M_o = \emptyset$ .

### **Equivalence**

Theorem: Every target based network can be transformed into a conventional network that accepts the same language.

TB-ANEP is called acceptance uniform if all nodes  $N_o = (M_o, I_o, O_o, B_o)$  with  $B_o \neq \emptyset$  satisfy  $M_o = \emptyset$ .

Lemma: Every target based network can be transformed into an acceptance uniform network that accepts the same language.

## **Acceptance Uniform TB-ANEP**

Lemma: Every target based network can be transformed into an acceptance uniform network that accepts the same language.

### **Acceptance Uniform TB-ANEP**

Lemma: Every target based network can be transformed into an acceptance uniform network that accepts the same language.

$$N'_{i} = (\emptyset, V^{*}, V^{*}, \emptyset),$$

$$N'_{i+n} = (M_{i}, V^{*}, V^{*}, \emptyset),$$

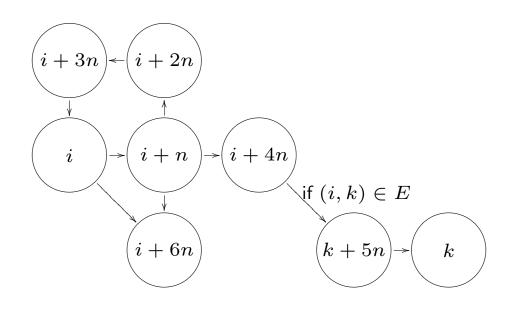
$$N'_{i+2n} = (\emptyset, V^{*} \setminus O_{i}, V^{*}, \emptyset),$$

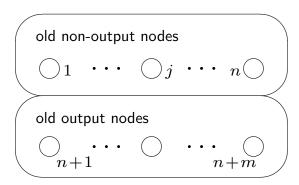
$$N'_{i+3n} = (\emptyset, V^{*}, V^{*}, \emptyset),$$

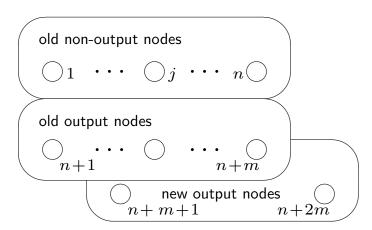
$$N'_{i+4n} = (\emptyset, O_{i}, V^{*}, \emptyset),$$

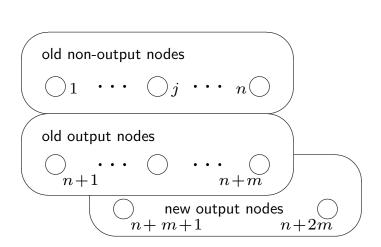
$$N'_{i+5n} = (\emptyset, I_{i}, V^{*}, \emptyset),$$

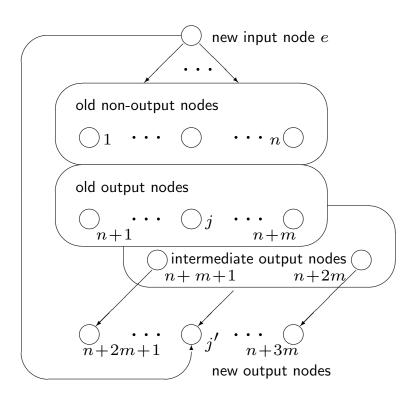
$$N'_{i+6n} = (\emptyset, B_{i}, V^{*}, B_{i}).$$











### Summary

Target based accepting networks and conventional ones have the same computational power.

The number of processors a target based network needs for accepting a language is not higher than the number of processors that a conventional network needs for the same language.